



ASSAM SCIENCE AND TECHNOLOGY UNIVERSITY
Guwahati
Course Structure and Syllabus

(From Academic Session 2018-19 onwards)

B.TECH
ELECTRICAL ENGINEERING

3rd SEMESTER



ASSAM SCIENCE AND TECHNOLOGY UNIVERSITY

Course Structure (From Academic Session 2018-19 onwards)

B.Tech 3rd Semester: Electrical Engineering

Semester III/ B. TECH/EE

Sl. No.	Sub-Code	Subject	Hours per Week			Credit	Marks	
			L	T	P	C	CE	ESE
Theory								
1	MA181301A	Mathematics III-A (for branches other than CSE and ECE/ETE)	2	1	0	3	30	70
2	EI181302	Electrical Circuit Analysis	3	1	0	4	30	70
3	EI181303	Analog Electronics	3	0	0	3	30	70
4	EI181304	Electrical Machines-I	3	1	0	4	30	70
5	EI181305	Digital Electronics	3	0	0	3	30	70
6	MC181306	Constitution of India	2	0	0	0 (PP/NP)	-	100
Practical								
1	EI181313	Analog Electronics Lab	0	0	2	1	15	35
2	EI181314	Electrical Machines-I Lab	0	0	3	1.5	15	35
3	EI181315	Digital Electronics Lab	0	0	2	1	15	35
4	SI181321	Internship-I (SAI - Social)	0	0	0	1	-	100
TOTAL			16	3	7	21.5	195	655
Total Contact Hours per week : 26								
Total Credits: 21.5								

N.B. MC181306 is a Mandatory Audit Course (No Credit). It will be evaluated as PP (Pass) or NP (Not Pass)

Detailed Syllabus:

Course Code	Course Title	Hours per week L-T-P	Credit C
MA181301A	Mathematics III-A (for branches other than CSE and ECE/ETE)	2-1-0	3

MODULE 1: Partial Differential Equation: (15 Hours)

Formation of Partial Differential equations, Linear partial differential equation of first order, Non-linear partial differential equations of first order, Charpit's method, Method of separation of variables, boundary value problem with reference to the one dimensional heat and wave equation.

MODULE 2: Probability Theory: (15 Hours)

Review of basic probability and Bayes' theorem, Probability distribution, Binomial, Poisson and normal distribution, Joint distribution, Test of significance, fitting of straight line by least square method, Elementary concept of Markov Chain.

MODULE 3: Laplace Transform: (10 Hours)

Laplace transform of elementary function, Properties of Laplace transform, inverse Laplace transform, convolution theorem, Solution of ordinary differential equations with the help of Laplace transform.

Textbooks/References:

1. Advanced Engineering Mathematics: Erwin Kreyszig
2. Higher Engineering Mathematics: B V Ramana
3. Theory and problems of Probability: Seymour Lipschutz
4. A text book of engineering Mathematics: N. P. Bali & M. Goel
5. Statistical Methods: An Introductory Text- J.Medhi, New Age International Publishers

Course Code	Course Title	Hours per week L-T-P	Credit C
EI181302	Electrical Circuit Analysis	3-1-0	4

Pre-requisite: Knowledge of basic electrical engineering

Course Objectives:

- To impart the basic knowledge about various elements of electric circuits.
- To inculcate the understanding about application of Network Theorems for solving circuits and modelling circuits using matrix representation.
- To understand network topology, transient phenomenon and two port networks.
- To be able to apply various techniques of networks synthesis from network functions.

Course Outcomes:

At the end of the course, the students will be able to

CO1:

Apply appropriate laws of various circuit elements to develop mathematical models, detailing their constructional features and their applications.

CO2:

Make use of various network theorems to evaluate variety of networks and to model given networks using appropriate matrix representation amenable to various types of network analysis.

CO3:

Model networks using elementary graph theory and to analyse given networks for their transient response.

CO4:

Utilize the concept of two port networks, their characteristics and mode of interconnection to evaluate various networks.

CO5: Synthesize networks from various network functions like admittance and impedance functions.

MODULE 1: Elements of electric circuits:

(5 Lectures)

resistor: Practical engineering devices. Solid & wire frame of common resistive materials, carbon film and metal film resistors. Heat dissipating area (wattage), tolerance and temperature stability of resistors. Mathematical models, the capacitor: Comparison of properties of different traditional & modern dielectric materials (e.g. paper, ceramic, polystyrene polycarbonates etc.) Short description of electrolytic capacitor- wet & solid dielectrics, solid tantalum & aluminium capacitors, Mathematical models, the inductors: Mutual inductance, Properties of core materials.

Brief studies of iron & ferrite cores, Mathematical models (only assignments).

MODULE 2: Network theorems: (8 Lectures)
Millman's theorem, Reciprocity theorem, Compensation & Tellegen's, etc. Analysis of coupled circuits. The dot rule & equivalent conductivity coupled forms of magnetically coupled circuits.

MODULE 3: Methods for the solution of circuit equations: (4 Lectures)
Analysis of electrical network with – independent/dependent ideal, practical current voltage and current sources – both of DC and AC. Solution of network equations by matrix methods.

MODULE 4: Network topology: (5 Lectures)
Elementary graph theory as applied to electrical networks. Matrices of graph: Incidence matrix, circuit matrix, cut set matrix. Advanced techniques of equation formulation for numerical solutions.

MODULE 5: Transient phenomenon: (7 Lectures)
Forcing functions-impulse, step and ramp functions Study and solution of simple circuits undergoing transient disturbances, A.C. transients, Time domain equations and solutions by Laplace transforms

MODULE 6: Two port Network: (7 Lectures)
General principles, ABCD, Z, Y and hybrid parameters, Analysis of networks in tandem. Transmission lines. Lumped and distributed models. Combination/Interconnection of two ports network.

MODULE 7: Introduction to Network Synthesis: (9 Lectures)
Realizability of networks, positive real function (PRF) and its properties, Hurwitz polynomial, Routh-Hurwitz Array, properties of various immittance functions (LC, RC, RL), Foster and Caur forms of realization of network for given driving point impedance/admittance function.

Text Books:

1. Basic Electrical Engineering—A Chakravarti, S Nath, C K Chanda, Tata Mcgrawhill, 2017
2. Network Analysis and Synthesis—Smarajit Ghosh, PHI, First Edition
3. Electric Circuit----J.A. Edminiter. (Mcgraw hill)
4. Networks and Systems ---D Roychowdhury, New Age International Publications, 1998
5. A course in Electric Circuit Analysis—Soni & Gupta

Reference Books:

1. Computer aided Network Design—Donald A. Calahan (Tata Mcgraw hill)
2. Engineering Circuit analysis---- W. H. Hayt Jr. & J.E. Kemmerly (Mcgraw hill)
3. Network Analysis---M E Van Valkenburg, PHI, 2006
4. Electric Circuits----C K Alexander and M N O Sadiku, McGraw Hill Education ,2004

Course Code	Course Title	Hours per week L-T-P	Credit C
EE181303	Analog Electronics	3-0-0	3

Course Objectives:

- To teach the basic concept and applications of various analog electronic devices and circuits.
- To develop problem formulation and problem solving ability amongst the students.

Course Outcomes: At the end of this course, the students will be able to

CO1:

Identify and analyze various types of diodes and their applications.

CO2:

Characterize and analyze the performance and applications of BJTs and FETs.

CO3:

Analyze the performance of cascaded amplifiers.

CO4:

Analyze the effects of Negative and Positive Feedback on amplifiers.

CO5:

Analyze and design basic OPAMP applications.

MODULE 1: The P-N Junction:

(8 Lectures)

Charge Flow Description and Energy Band Description of P-N Junction. Shockley's Equation. Resistance and Capacitances associated with a P-N Junction. Piecewise linear model of Diode. Effect of temperature.

Practical applications of diodes, Rectifiers and Filters, Clippers, Clampers, Voltage Multipliers.

Special Purpose Diodes- Zener Diode, LED, Photo-Diode, Varactor Diode, Tunnel Diode, Schottky Diode.

MODULE 2: Bipolar Junction Transistor:

(12 Lectures)

Construction and Principle of operation of BJT. Transistor Action. Transistor Current Equations, ' α ' and ' β ' parameters. Transistor Characteristics. Early Effect. Ebers-Moll Model and Justification of Transistor Characteristics. Concept of Amplification and Switching Action. Transistor Biasing Circuits. Stability of operation and Stability Factor

Small signal r_e -parameter model and h-parameter model of the BJT. Introduction to π - model. Analysis of amplifiers with small signal models. Current Gain, Voltage Gain, Power Gain, Input Impedance and Output Impedance. Introduction to Miller's Theorem. Cascaded Amplifier. Coupling between stages, Frequency Response and Band Width of an amplifier.

Darlington Connection, Cascode Connection and Difference Amplifier Connection of Transistors.

MODULE 3: Field Effect transistors, JFET and MOSFET: (5 Lectures)

Basic construction and physical behaviour of JFET and MOSFET. Study of current flow through JFET and MOSFET. JFET and MOSFET Characteristics- Drain Characteristics and Transfer Characteristics. Biasing circuits for JFET and MOSFET. Small Signal Model of JFET. Analysis of JFET amplifier.

JFET and MOSFET switches.

MODULE 4: Effect of Negative and Positive Feedback in Amplifiers: (5 Lectures)

Effect of Negative Feedback in amplifiers and its advantages. Analysis of different feedback configurations.

Positive Feedback. Barkhausen Criteria for oscillation. Analysis of RC Oscillator, LC Oscillator and Crystal Oscillator.

MODULE 5: Integrated Circuit Electronics: (10 Lectures)

Internal Circuit Diagram of OPAMPs, TTL Gates and CMOS Gates.

Functional Block Diagram and Ideal Characteristics of OPAMP, Open-Loop Gain, Input and Output Impedance, Bandwidth, CMRR, Slew Rate, Input Offset Voltage and Current.

Closed Loop application of OPAMP, - Non-Inverting and Inverting Configurations. Concept of Virtual Ground. Non-inverting and Inverting Amplifiers. Adders, Subtractors, Difference Amplifiers and Instrumentation Amplifier. Integrators and Differentiators. Log Amplifier. OPAMP Sinusoidal Oscillators.

Non-Linear Applications – Comparator, Schmitt trigger, Zero Crossing Detector, A stable Multivibrator.

Active Filters, LPF, HPF, BPF and Notch Filters.

Introduction to Timer IC 555 and its applications.

Textbooks/References:

1. Integrated Electronics - Millman & Halkias (PHI).
2. OPAMPs and Linear Integrated Circuits- Ramakant A. Gayakwad (PHI)
3. Electronic Devices and Circuits – David J Bell. (Oxford)

Course Code	Course Title	Hours per week L-T-P	Credit C
EE181304	Electrical Machines-I	3-1-0	4

Prerequisites:

- Basic knowledge of electrical and magnetic circuits and electromagnetic induction

Objectives:

To give an idea about the principle of operation, working and performance of D.C. machines and transformers

Course Outcomes: At the end of the course, the students will be able to:

CO1:

Understand the principles of electromagnetic energy conversion and electromagnetic induction

CO2:

Understand the principles of operation and construction of D.C. machines (both motors and generators) and transformers

CO3:

Analyze the various characteristics of D.C. generators and D.C. motors, and various connections of three-phase transformers

CO4:

Interpret the necessary conditions to be satisfied for parallel operation of D.C. Generators and single phase and three phase transformers

CO5:

Evaluate the performances of DC machines and transformers by carrying out various tests on them

MODULE 1: (i) Electromagnetic Induction (2 Lectures)
(ii) Electromechanical Energy Conversion (2 Lectures)

Review of magnetic circuits, MMF, Electromagnetic induction, magnetically induced e.m.f., Magnetically coupled circuits

Basic principles of energy conversion, Field magnets, Dynamically induced e.m.f. and torque in rotating machines

MODULE 2: D.C. Machines

(i) Generators: (12 Lectures)

Constructional features, Details of Armature windings, Methods of excitations-shunt, series and compound. E M F equation, Armature reaction, Inter-poles and compensatory windings, Commutation, Characteristic of generators, Efficiency and Regulation, Parallel operation

(ii) Motors: (8 Lectures)

Torque equation of motors, Speed and Torque characteristic curves of shunt, series and compound motors, Starting of D C motors – Starters and grading of starting resistance, Speed control-

conventional methods and solid state control, Choice of motors for different duties, Losses and efficiency, Testing – Swinburne’s test, Back to back test, Retardation test and Brake test.

MODULE 3: Transformers:

(8 Lectures)

Principles of operation of transformer, voltage and current ratios, Construction – shell type and core type, single phase and poly phase cooling methods, E.m.f. equation and output equation, Magnetic circuit, leakage flux and leakage reactance, Phasor diagram, per unit values of resistance and reactance, Open circuit and short tests, back to back test, Regulation, losses and efficiency, maximum efficiency, all-day efficiency, Auto-transfer, 3-phase transformer, Phase transformation and connections. Parallel operation of transformers, Vector grouping, Harmonics.

Text Books:

1. Nagrath D.P. & Kothari I.J, " Electrical Machines", Tata McGraw Hill Education
2. Bimbra, P.S., "Electric Machinery", Khanna Publishers
3. Mehta V.K. and Mehta, R. "Principle of Electrical Machines", S. Chand and Co.

Reference Books:

1. Langsdorf A.S: Theory of Alternating Current Machinery, McGraw Hill Education
2. Chapman.J, “Electric Machinery Fundamentals”, McGraw Hill Book Co.
3. Fitzgerald, A.E., Charles Kingsely Jr. Stephen D. Umans, “Electric Machinery” McGraw Hill Books Company
4. J.B. Gupta, "Theory & Performance of Electrical Machines" Katsons Books

Course Code	Course Title	Hours per week L-T-P	Credit C
EI181305	Digital Electronics	3-0-0	3

Prerequisites: Basic idea of Electrical and electronics circuits

Course Objectives:

- To understand number representation and conversion between different representation in digital electronic circuits.
- To understand and implement logical operations using combinational logic circuits.
- To understand and implement logical operations using sequential logic circuits.
- To understand characteristics of memory and their classification.

Course Outcomes:

At the end of this course, the students will be able to

- CO1:** Perform conversion of n-bit codes from one form to another form.
- CO2:** Design a logical circuit using the minimum number of gates for a given logical expression.
- CO3:** Use and demonstrate a given digital combinational circuit.
- CO4:** Use different types of flip-flops and design a sequential logic circuit.
- CO5:** Design ROM, RAM and PLA for a given application.

MODULE 1: Number system (4 Lectures)

Representation of Binary numbers, octal and hexadecimal numbers, complements, signed binary numbers, Binary codes, code conversion, floating point numbers and arithmetic and the conversion process.

MODULE 2: Boolean algebra and logic gates (4 Lectures)

Basic theorems and properties of Boolean algebra, Boolean functions, canonical and standard forms- SOP & POS. Logical operations, truth tables, logic gates, logic levels and pulse waveforms.

MODULE 3: Simplification of Boolean functions (6 Lectures)

The map method- the Karnaugh map, minimal SOP & POS, Don't care conditions, multiple output minimization, tabular method, Quine-Mcclusky method, determination and selection of prime implicants.

MODULE 4: Combinational Logic circuits (9 Lectures)

Introduction: Logic synthesis: Introduction, universal property of NAND and NOR gates, AND-OR networks, NAND & NOR networks, EX-OR networks.

Adders & subtractors, parallel binary adders, magnitude comparator, decoders & encoders, Multiplexer & demultiplexers, parity generators & checkers. ROM, RAM and PLA.

MODULE 5: Sequential Logic Circuits**(9 Lectures)**

S-R, J-K, D and T Flip Flops, Excitation table, Triggering of FFs & Latches, one shot A stable Multivibrator. Registers: - Shift – Registers, Ripple Counters, Synchronous Counters. Ring Counters, Timing Sequences, Design Procedure.

MODULE 6: Digital integrated Circuit**(4 Lectures)**

Introduction, special characteristics (Fan-Out, Power dissipation, Propagation delay, figure of merit, noise level) Introduction to TTL, ECL, MOS, and CMOS circuit.

MODULE 7: A/D and D/A converters**(4 Lectures)**

Introduction and examples of ADC and DAC circuits.

Text Books:

1. Digital Design – M. Marris Mano.
2. Logic Design Theory – NN Biswas
3. Digital Fundamental – TL Floyd
4. Digital Electronics- R. P. Jain.

Reference Books:

1. Logic Design Theory – NN Biswas
2. Digital Fundamental – TL Floyd

Course Code	Course Title	Hours per week L-T-P	Credit C
MC181306	Constitution of India	2-0-0	0 (PP/NP)

Course Objectives: Students will be able to:

1. Understand the premises informing the twin themes of liberty and freedom from a civil rights perspective.
2. To address the growth of Indian opinion regarding modern Indian intellectuals' constitutional role and entitlement to civil and economic rights as well as the emergence of nationhood in the early years of Indian nationalism.
3. To address the role of socialism in India after the commencement of the Bolshevik Revolution in 1917 and its impact on the initial drafting of the Indian Constitution.

Course Outcomes: Students will be able to:

1. Discuss the growth of the demand for civil rights in India for the bulk of Indians before the arrival of Gandhi in Indian politics.
2. Discuss the intellectual origins of the framework of argument that informed the conceptualization of social reforms leading to revolution in India.
3. Discuss the circumstances surrounding the foundation of the Congress Socialist Party [CSP] under the leadership of Jawaharlal Nehru and the eventual failure of the proposal of direct elections through adult suffrage in the Indian Constitution.
4. Discuss the passage of the Hindu Code Bill of 1956.

MODULE 1: History of Making of the Indian Constitution:

- a) History
- b) Drafting Committee, (Composition & Working)

MODULE 2: Philosophy of the Indian Constitution:

- a) Preamble
- b) Salient Features

MODULE 3: Contours of Constitutional Rights & Duties:

- a) Fundamental Rights
- b) Right to Equality
- c) Right to Freedom
- d) Right against Exploitation
- e) Right to Freedom of Religion
- f) Cultural and Educational Rights
- g) Right to Constitutional Remedies Directive Principles of State Policy Fundamental Duties.

MODULE 4: Organs of Governance:

- a) Parliament
- b) Composition
- c) Qualifications and Disqualifications
- d) Powers and Functions
- e) Executive
- f) President
- g) Governor
- h) Council of Ministers
- i) Judiciary, Appointment and Transfer of Judges, Qualifications
- j) Powers and Functions

MODULE 5: Local Administration:

- a) District's Administration head: Role and Importance,
- b) Municipalities: Introduction, Mayor and role of Elected Representative CEO of Municipal Corporation.
- c) Pachayati raj: Introduction, PRI: Zila Pachayat.
- d) Elected officials and their roles, CEO Zila Pachayat: Position and role.
- e) Block level: Organizational Hierarchy (Different departments),
- f) Village level: Role of Elected and Appointed officials,
- g) Importance of grass root democracy

MODULE 6: Election Commission:

- a) Election Commission: Role and Functioning.
- b) Chief Election Commissioner and Election Commissioners.
- c) State Election Commission: Role and Functioning.
- d) Institute and Bodies for the welfare of SC/ST/OBC and women.

Textbooks/References:

1. The Constitution of India, 1950 (Bare Act), Government Publication.
2. Dr. S. N. Busi, Dr. B. R. Ambedkar framing of Indian Constitution, 1st Edition, 2015.
3. M. P. Jain, Indian Constitution Law, 7th Edn., Lexis Nexis, 2014.
4. D.D. Basu, Introduction to the Constitution of India, Lexis Nexis, 2015.

Course Code	Course Title	Hours per week L-T-P	Credit C
EI181313	Analog Electronics Lab	0-0-2	1

Course Objectives:

1. To understand the basic concept of various electronic devices, circuits and their application.
2. To develop ability among students to design and implement electronic circuits.

Course Outcomes:

After completing this course, the students will be able

1. To determine the dc and ac parameters of semiconductor devices.
2. To analyze the performance of different types of rectifier with and without filter.
3. To plot frequency response of BJT and JFET amplifier.
4. To analyze effect of negative and positive feedback on the performance of amplifier.

LIST OF EXPERIMENTS

1. Study of Rectifier and Filter
2. Common Base Characteristics and Evaluation of H-Parameters.
3. Common Emitter Characteristics and Evaluation of H- Parameters.
4. Study of R-C Coupled Common Emitter Amplifier and Effect of Feedback.
5. Study of JFET Characteristics and Common Source Amplifier.
6. Study of OPAMP Inverting and Non-Inverting Amplifiers.
7. Study of OPAMP Summing and Difference Amplifiers and Instrumentation Amplifier.
8. Study of OPAMP Phase-Shift Oscillator and Wien Bridge Oscillator.
9. Study of OPAMP Active Filters.
10. Study of Non-Linear Opamp Applications.

Course Code	Course Title	Hours per week L-T-P	Credit C
EI181314	Electrical Machines-I Lab	0-0-3	1.5

Course Objectives:

The Electrical Machines-I Laboratory is designed to provide the students with the practical knowledge of electrical machines specifically keeping in view the following objectives:

1. to get hands-on experience in performing the basic tests on electrical machines
2. to reinforce the theoretical concepts with related practical understanding
3. to know about the various precautionary measures necessary in handling electrical machines
4. to develop technical report writing skill

Course Outcome:

After completing this course, the students will

CO1:

be familiar with the mode of starting, switching-off, and taking precautionary measures while handling electrical machines

CO2:

be able to reinforce their theoretical concepts by way of experimentation

CO3:

develop report writing skill

LIST OF EXPERIMENTS

1. O.C.C. of D.C. generators
2. Load Test on D.C. shunt generators
3. Speed Control of D.C. shunt motors
4. Open circuit and short circuit test on single-phase transformers
5. Load test on single-phase transformers
6. Sumpner's Test or back-to-back test on two similar single-phase transformers
7. Three-phase Transformer Connections
8. Hopkinson's or Back-to-back test on two similar D.C. Machines

Course Code	Course Title	Hours per week L-T-P	Credit C
EI181315	Digital Electronics Lab	0-0-2	1

Course outcomes:

At the end of the course, the students will be able to:

CO1:

verify and analyze the outputs of combinational logic circuits and relate theoretical concepts with experimental analysis.

CO2:

verify and analyze the outputs of sequential logic circuits and relate theoretical concepts with experimental analysis.

CO3:

organize and write an engineering report after performing an experiment on digital circuits.

LIST OF EXPERIMENTS

1. Realization of basic gates by using universal gates
2. Realization of XOR gate
3. Combinational Logic Design using 74xx ICs
4. Arithmetic Circuit- construction and testing using 74xxICs: Half/Full Adder
5. Construction of 1- bit comparator using 74xxICs.
6. code converters – Binary to Gray & Gray to binary.
7. Verification of Truth Table of SR Flip-Flop
8. Verification of Truth Tables of JK, D, T Flip-Flops
9. 3-Bit synchronous counter design
10. Decade Counter design
